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From: Abdul Zindani  
Texas Instruments Incorporated  
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JAN 05 2006

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of

Bhupendra Sharma, et al.

Docket No.: TI-38242

Serial No.: 10/711,532

Art Unit: 2816

Filed: September 24, 2004

Examiner: Anh Quan Tra

Customer No. 23494

Conf. No.: 5531

For: Avoiding Excessive Cross-Terminal Voltages of Low Voltage Transistors Due to  
Undesirable Supply-Sequencing in Environments With Higher Supply Voltages

CERTIFICATION OF FACSIMILE TRANSMISSION

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FACSIMILE COVER SHEET

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<input type="checkbox"/> <b>NEW APPLICATION</b>	<input type="checkbox"/> <b>EOT</b> _____ (# Page)
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<input type="checkbox"/> <b>DIVISIONAL APPN</b>	
<b>NAME OF INVENTOR(S):</b> Bhupendra Sharma, et al.	
<b>RECEIPT DATE &amp; SERIAL NO.:</b> <b>Serial No.: 10/711,532</b> <b>Filing Date: September 24, 2004</b>	
<b>TITLE OF INVENTION:</b> Avoiding Excessive Cross-Terminal Voltages of Low Voltage Transistors Due to Undesirable etc.	
<b>TI FILE NO.:</b> <b>TI-38242</b>	<b>DEPOSIT ACCT. NO.:</b> <b>20-0668</b>
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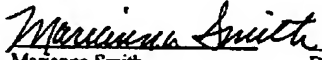
Texas Instruments Incorporated  
PO Box 655474, M/S 3999  
Dallas, TX 75265

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

JAN 05 2006

In re the Application of:

	Bhupendra Sharma, et al.	Docket No.: TI-38242
Serial No:	10/711,532	Art Unit: 2816
Filed:	September 24, 2004	Examiner: Anh Quan Tra
Customer No.:	23494	Conf. No.: 5531
Title:	Avoiding Excessive Cross-Terminal Voltages of Low Voltage Transistors Due to Undesirable Supply-Sequencing in Environments With Higher Supply Voltages	

CERTIFICATION OF FAX TRANSMITTAL	
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Commissioner for Patents  
P.O. Box 1450  
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**AMENDMENT PURSUANT TO 37 CFR 1.111**

Dear Sir:

This amendment is in response to the Office Action dated October 13, 2005, having a statutory period for response set to expire on January 13, 2006.

Amendments to Claims begin on page 2.

Remarks/Arguments begin on page 6.

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